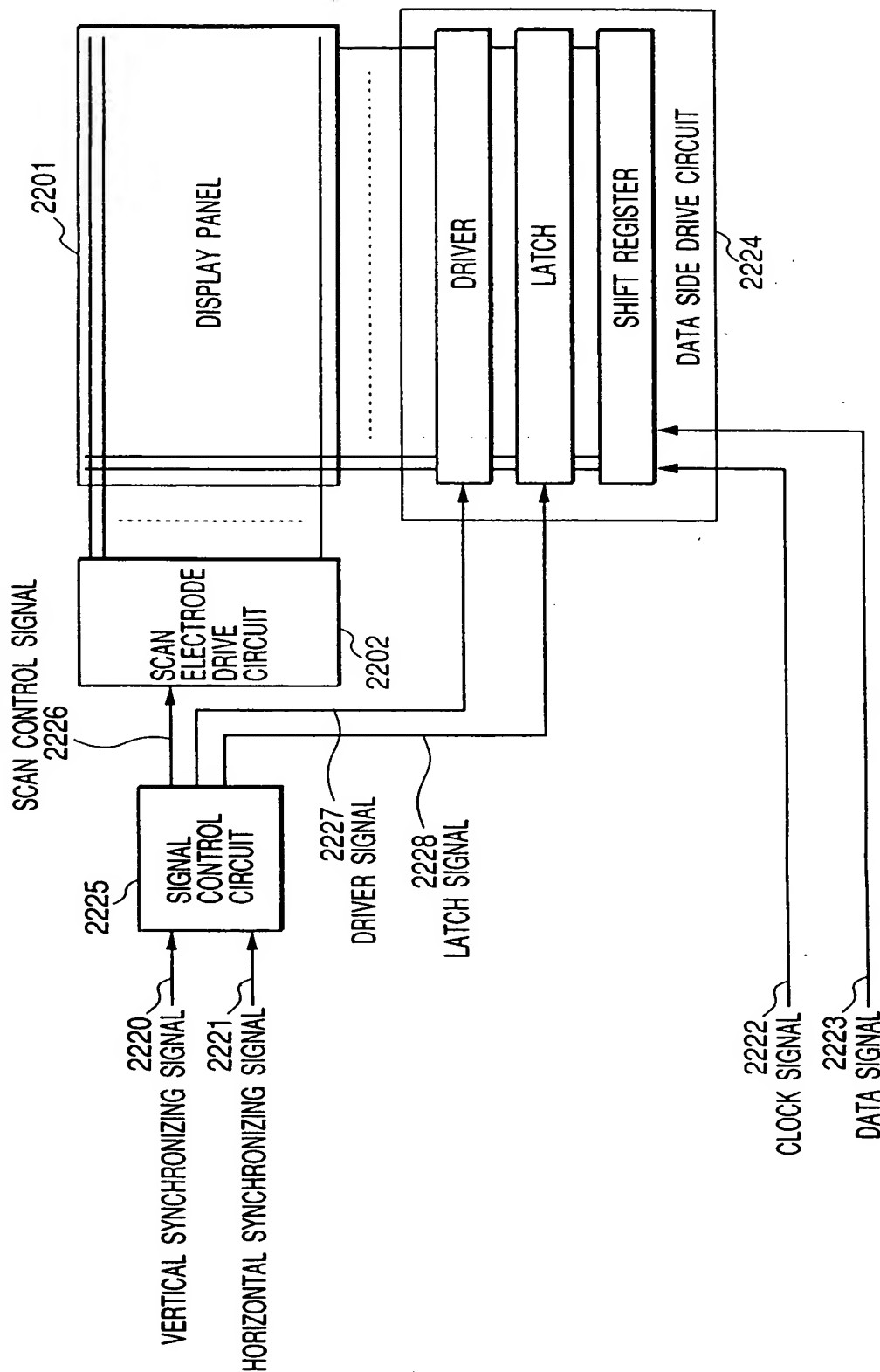




FIG. 22
(PRIOR ART)



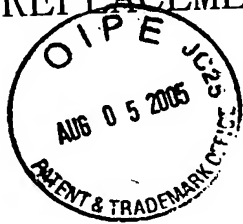


FIG. 23
 (PRIOR ART)

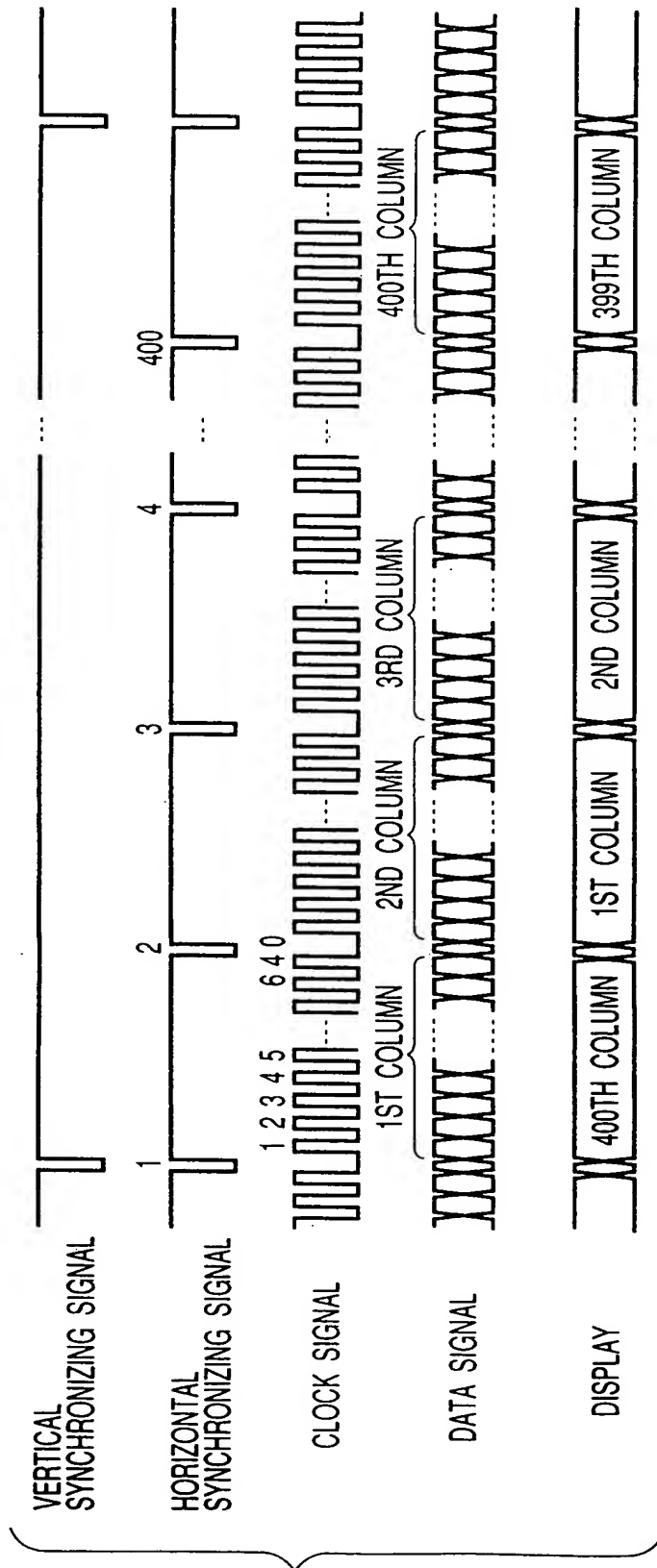


FIG. 24
(PRIOR ART)

The diagram illustrates a video display system architecture. At the top, a **DISPLAY PANEL** (2401) is connected to a **SCAN ELECTRODE DRIVE CIRCUIT** (2402) and a **DATA SIDE DRIVE CIRCUIT** (2403). The **SCAN ELECTRODE DRIVE CIRCUIT** (2402) receives a **SCAN CONTROL SIGNAL** (2407) and a **DRIVER SIGNAL** (2408). The **DATA SIDE DRIVE CIRCUIT** (2403) consists of four columns, each containing a **DRIVER** (1-4), a **LATCH** (1-4), and a **SHIFT REGISTER** (1-4). These are connected to the display panel via a **DATA TRANSFER SIGNAL** (2410). A **SIGNAL CONTROL CIRCUIT** (2405) receives **VERTICAL SYNCHRONIZING SIGNAL** (2420) and **HORIZONTAL SYNCHRONIZING SIGNAL** (2421), and outputs a **LATCH SIGNAL** (2409) to the **DATA SIDE DRIVE CIRCUIT** (2403). A **CLOCK DIVISIONAL CIRCUIT** (2406) receives a **CLOCK SIGNAL** (2422) and outputs a **CLOCK DIVISIONAL CONTROL SIGNAL** (2411) to the **DATA SIDE DRIVE CIRCUIT** (2403). The **DATA SIDE DRIVE CIRCUIT** (2403) also receives **TRANSFER DATA 1** (2416), **TRANSFER DATA 2** (2417), **TRANSFER DATA 3** (2418), and **TRANSFER DATA 4** (2419) from a **RECORDING CIRCUIT UNIT** (2404). The **RECORDING CIRCUIT UNIT** (2404) includes **MEMORY 1** through **MEMORY 4**, which are connected to **DIVISIONAL CLOCK SIGNAL 1** (2412), **DIVISIONAL CLOCK SIGNAL 2** (2413), **DIVISIONAL CLOCK SIGNAL 3** (2414), and **DIVISIONAL CLOCK SIGNAL 4** (2415) respectively. A **DATA SIGNAL** (2423) is also provided to the **RECORDING CIRCUIT UNIT** (2404).



FIG. 25
 (PRIOR ART)

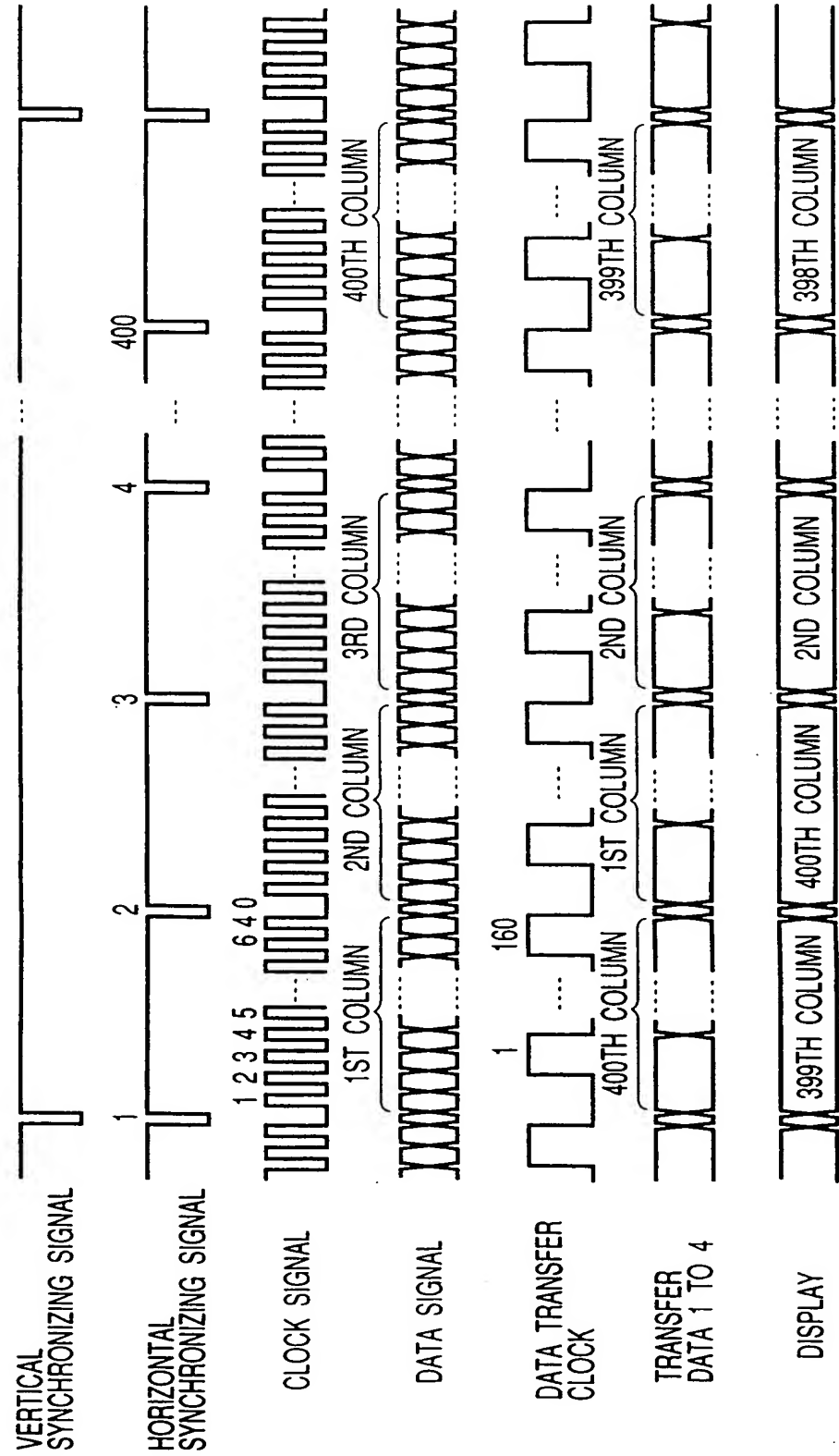
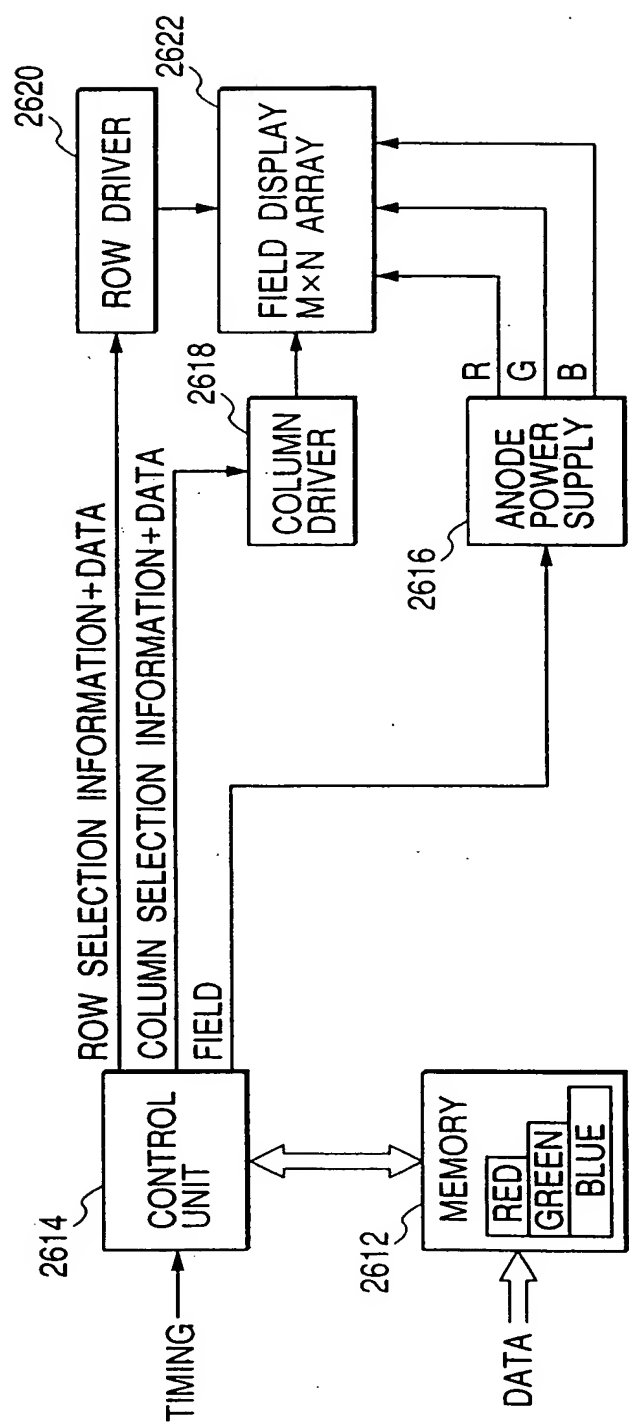
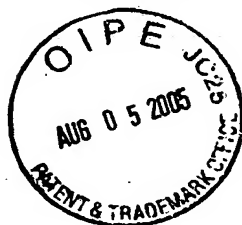




FIG. 26
 (PRIOR ART)

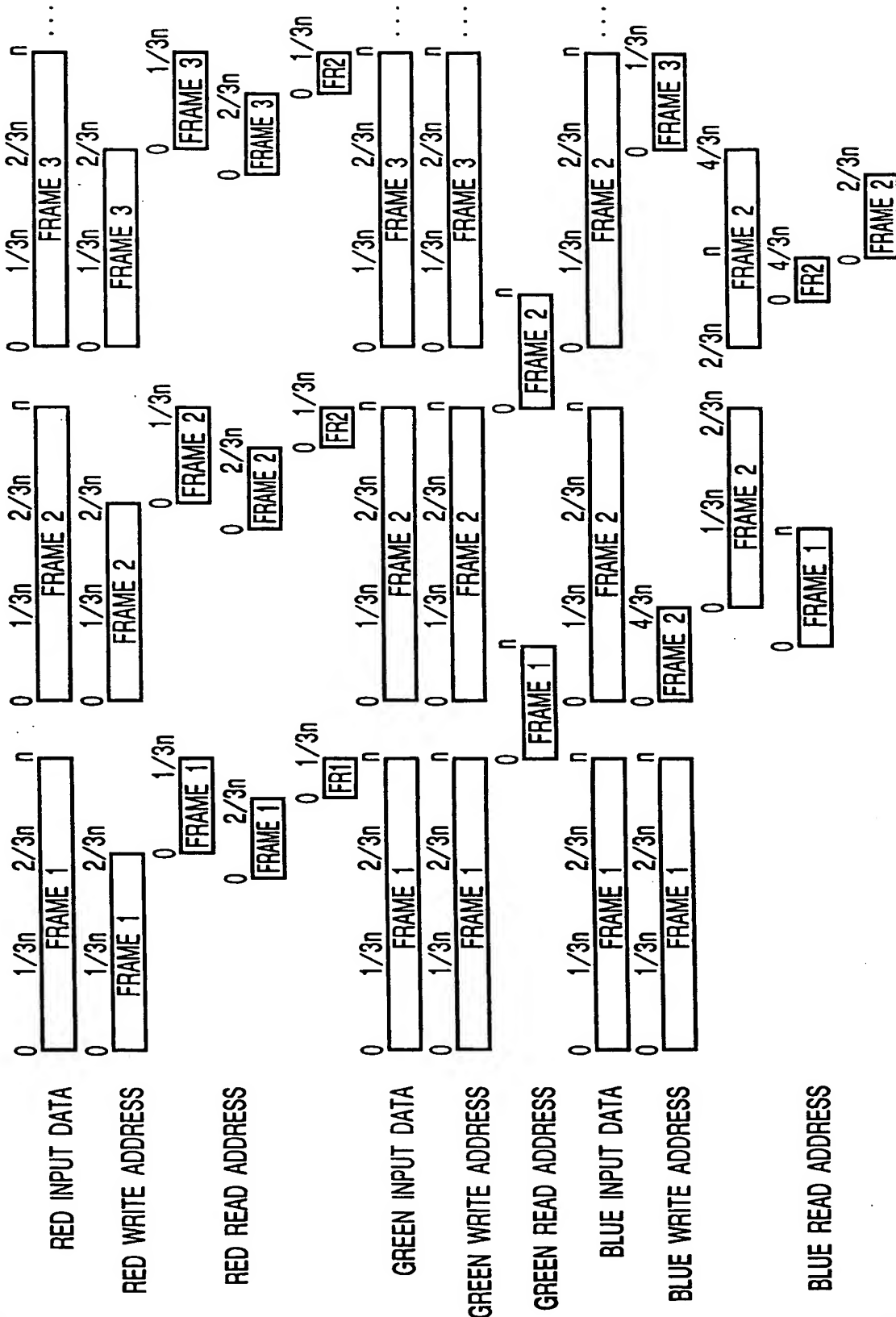


REPLACEMENT SHEET



U.S. PATENT APPLICATION
INVENTOR: MUNIKE ANDO
TITLE: IMAGE DISPLAY APPARATUS
Sheet 6 of 8
Docket No.: 03500.016121
Appln. No.: 10/054,895 Filed: 01/25/2002

FIG. 27 (PRIOR ART)



REPLACEMENT SHEET

U.S. PATENT APPLICATION
 INVENTOR: MUNIKE ANDO
 TITLE: IMAGE DISPLAY APPARATUS
 Sheet 7 of 8
 Docket No.: 03500.016121
 Appln. No.: 10/054,895 Filed: 01/25/2002

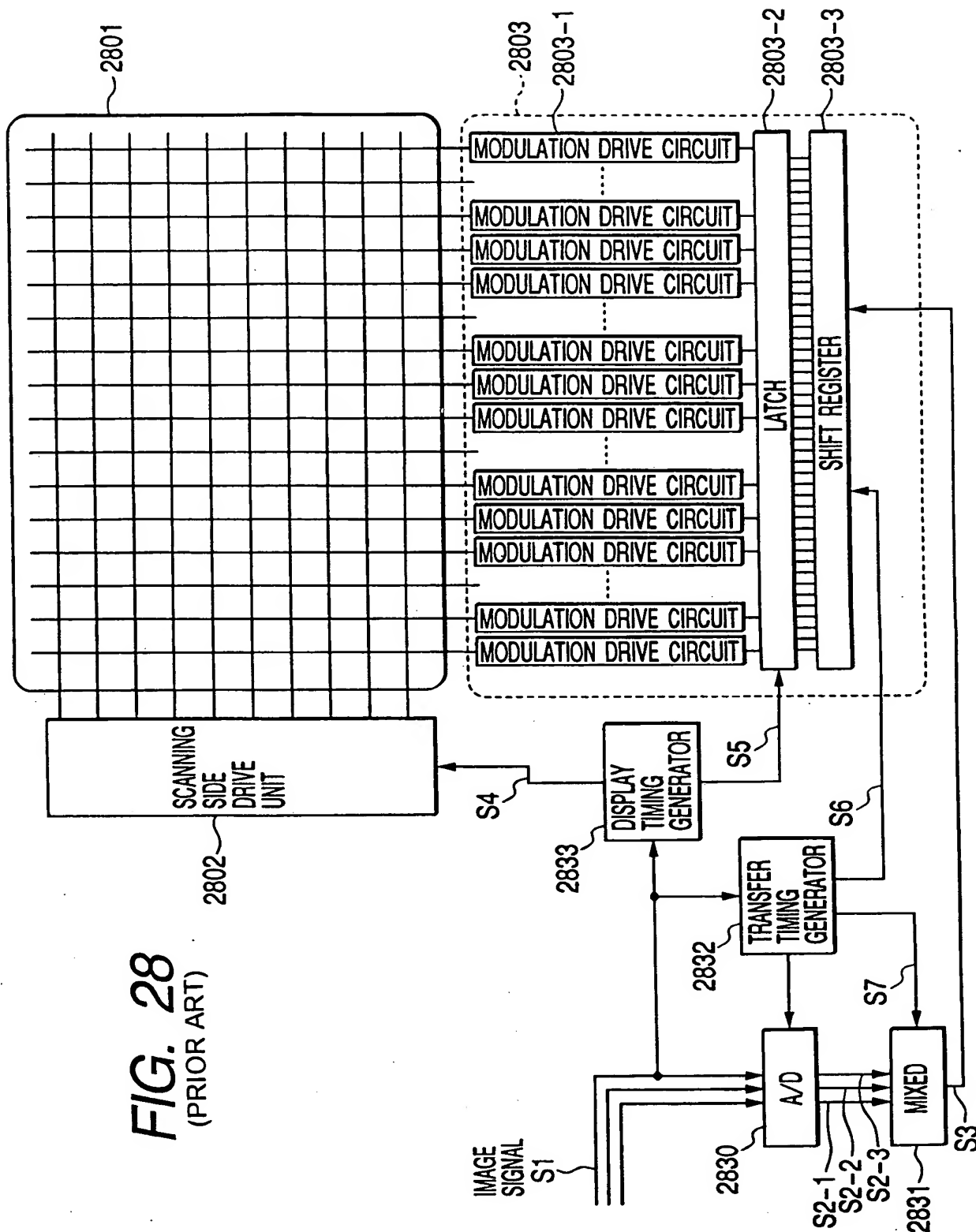


FIG. 28
(PRIOR ART)

REPLACEMENT SHEET

U.S. PATENT APPLICATION
 INVENTOR: MUNIKE ANDO
 TITLE: IMAGE DISPLAY APPARATUS
 Sheet 8 of 8
 Docket No.: 03500.016121
 Appln. No.: 10/054,895 Filed: 01/25/2002

FIG. 29 (PRIOR ART)

